EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	8503	(716/1-6,18.ccls 714/11,12,25-31, 48-57,732-738.ccls.)	US-PGPUB; USPAT	OR	ON	2006/08/04 11:00
L3	513	voter and (redundancy or fault$2 adj2 toleran$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/04 11:01
L4	99	1 and 3	US-PGPUB; USPAT	OR	ON	2006/08/04 11:05
L5	18	4 and (HDL or VHDL or Verilog or RTL)	US-PGPUB; USPAT	OR	ON	2006/08/04 11:03
L6	30	(majority adj2 voter) same fault	US-PGPUB; USPAT	OR	ON	2006/08/04 11:06

Web   Images   Groups   News   Froogle   Maps   **more »**

Google™ | fault tolerance redundant voter | Search   Advanced Search / Preferences

Web   Results **1 - 10** of about **487,000** for <u>fault</u> <u>tolerance</u> <u>redundant</u> <u>voter</u> . (0.61 seconds)

Fault-tolerant system - Wikipedia, the free encyclopedia
Fault-tolerance or graceful degradation is the property that enables a system to ... is an
example of a **fault-tolerant** storage device that uses **redundancy**. ...
en.wikipedia.org/wiki/Graceful_degradation - 21k - Cached - Similar pages

CS449/549
Welcome to **Fault-Tolerant** Systems CS449/549. This course is offered in the Fall
Semester ... Error Recovery, **Fault** Treatment, Passive HW **Redundancy, Voting**. ...
www.cs.uidaho.edu/~krings/CS449/index.html - 23k - Cached - Similar pages

Amazon.com: Reliability of Computer Systems and Networks: **Fault** ...
Reliability of Computer Systems and Networks: **Fault Tolerance**, Analysis, and Design ...
ordinary parallel system, **redundant voters, voter** reliability, ...
www.amazon.com/exec/obidos/tg/detail/-/0471293423?v=glance - 97k -
Cached - Similar pages

[PDF] Secure and **Fault-Tolerant Voting** in Distributed Systems
File Format: PDF/Adobe Acrobat - View as HTML
Distributed **voting** is a well-known **fault-tolerance** tech-. nique [4]. For the most part, ...
output of **redundant voters** in a wide area network. However, ...
www.cse.buffalo.edu/.../publications/Secure%20and%20fault-tolerant%20voting%20in%
20distributed%20systems.pdf - Similar pages

[PPT] MISSION / SAFETY CRITICAL ARCHITECTURES INTRUSION **TOLERANT** ...
File Format: Microsoft Powerpoint - View as HTML
N-version programming with confidence **voter**, layered on **Fault Tolerant** Processor ...
Quadruply **redundant fault tolerant** processor with 4 attached processors ...
www.tolerantsystems.org/Williamsburg/Mission_Safety.ppt - Similar pages

fault-tolerant system: Information From Answers.com
fault-tolerant system This article contains specific implementations of **fault** ... is an
example of a **fault-tolerant** storage device that uses **redundancy**. ...
www.answers.com/topic/**fault**-tolerant-system - 41k - Cached - Similar pages

[PDF] **FAULT-TOLERANT** COMPUTING Basic Concepts
File Format: PDF/Adobe Acrobat - View as HTML
Fault-tolerant computing is the art and science of building computing systems that ...
modules in a **redundant** triplet create errors so that the **vote** is no ...
www.cs.ucla.edu/~rennels/article98.pdf - Similar pages

[PDF] **FAULT TOLERANCE** WITH MULTIPLE TASK MODULAR **REDUNDANCY** Christopher ...
File Format: PDF/Adobe Acrobat - View as HTML
Hardware **fault tolerance** is usually achieved by. introducing hardware **redundancy** either
in the form. of modular **redundancy** with **voting** or of dynamic ...
profs.logti.etsmtl.ca/cfuhrman/Example_Paper.pdf - Similar pages

Citations: A theoretical investigation of generalized **voters** ...
A theoretical investigation of generalized **voters** for **redundant** systems. In The 19th
International Symposium on **Fault-Tolerant** Computing (FTCS 19), ...
citeseer.ist.psu.edu/context/466069/0 - 16k - Cached - Similar pages

IEEE *Xplore* ℗
RELEASE 2.1

Welcome United States Patent and Trademark Office

☐ Search Results   BROWSE   SEARCH   IEEE XPLORE GUIDE   SUPPORT

Results for "((fault tolerance<in>metadata) <and> (voter<in>metadata))<and> (redun..."   ☑ e-mail 🖨 printer friendly
Your search matched **11** of **1382205** documents.
A maximum of **100** results are displayed, **25** to a page, sorted by **Relevance** in **Descending** order.

» Search Options

View Session History

New Search

» Key

IEEE JNL	IEEE Journal or Magazine
IEE JNL	IEE Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IEE CNF	IEE Conference Proceeding
IEEE STD	IEEE Standard

Modify Search

((fault tolerance<in>metadata) <and> (voter<in>metadata))<and> (redundant<in⌐   [Search |>]

☐ Check to search only within this results set

Display Format: ◉ Citation   ◯ Citation & Abstract

[view selected items]   Select All Deselect All

☐   1. **Ratioed voter circuit for testing and fault-tolerance in VLSI processing arrays**
Belabbes, N.-E.; Guterman, A.J.; Savaria, Y.; Dagenais, M.;
Circuits and Systems I: Fundamental Theory and Applications, IEEE Transactions on [see also Circuits and Systems I: Regular Papers, IEEE Transactions on]
Volume 43, Issue 2, Feb. 1996 Page(s):143 - 152
Digital Object Identifier 10.1109/81.486436

AbstractPlus | References | Full Text: PDF(848 KB)   IEEE JNL
Rights and Permissions

☐   2. **Fault tolerant nanoelectronic processor architectures**
Wenjing Rao; Orailoglu, A.; Ramesh Karri;
Design Automation Conference, 2005. Proceedings of the ASP-DAC 2005. Asia and South Pacific
Volume 1, 18-21 Jan. 2005 Page(s):311 - 316 Vol. 1
Digital Object Identifier 10.1109/ASPDAC.2005.1466180

AbstractPlus | Full Text: PDF(424 KB)   IEEE CNF
Rights and Permissions

☐   3. **Towards the fault tolerant software: fuzzy extension of crisp equivalence voters**
Manic, M.; Frincke, D.;
Industrial Electronics Society, 2001. IECON '01. The 27th Annual Conference of the IEEE
Volume 1, 29 Nov.-2 Dec. 2001 Page(s):84 - 89 vol.1
Digital Object Identifier 10.1109/IECON.2001.976459

AbstractPlus | Full Text: PDF(562 KB)   IEEE CNF
Rights and Permissions

☐   4. **On the optimal design of triple modular redundancy logic for SRAM-based FPGAs**
Kastensmidt, F.L.; Sterpone, L.; Carro, L.; Reorda, M.S.;
Design, Automation and Test in Europe, 2005. Proceedings
2005 Page(s):1290 - 1295 Vol. 2
Digital Object Identifier 10.1109/DATE.2005.229

AbstractPlus | Full Text: PDF(168 KB)   IEEE CNF
Rights and Permissions

☐   5.
Fault-tolerant linear finite state machines
Hadjicostis, C.N.; Verghese, G.C.;
Electronics, Circuits and Systems, 1999. Proceedings of ICECS '99. The 6th IEEE International Conference on
Volume 2, 5-8 Sept. 1999 Page(s):1085 - 1088 vol.2

Digital Object Identifier 10.1109/ICECS.1999.813422

AbstractPlus | Full Text: PDF(376 KB)   IEEE CNF
Rights and Permissions

6. **Ratioed voter circuit for testing and fault-tolerance in VLSI processing arrays**
Belabbes, N.; Guterman, A.; Savaria, Y.; Dagenais, M.;
Circuits and Systems, 1992. ISCAS '92. Proceedings., 1992 IEEE International Symposium on
Volume 3, 3-6 May 1992 Page(s):1125 - 1128 vol.3
Digital Object Identifier 10.1109/ISCAS.1992.230281

AbstractPlus | Full Text: PDF(340 KB)   IEEE CNF
Rights and Permissions

7. **Assessing the reliability and safety of fault tolerant designs**
Radu, M.;
Electronics Technology: Concurrent Engineering in Electronic Packaging, 2001. 24th
International Spring Seminar on
5-9 May 2001 Page(s):56 - 58
Digital Object Identifier 10.1109/ISSE.2001.931010

AbstractPlus | Full Text: PDF(164 KB)   IEEE CNF
Rights and Permissions

8. **VHDL modeling and analysis of error-control specific circuits for multiple-modular**
redundant systems with concurrent error location capability
Jiang Jianhui; Min Yinghua; Peng Chenglian;
ASIC, 2001. Proceedings. 4th International Conference on
23-25 Oct. 2001 Page(s):570 - 573
Digital Object Identifier 10.1109/ICASIC.2001.982627

AbstractPlus | Full Text: PDF(431 KB)   IEEE CNF
Rights and Permissions

9. **Fault-tolerant dynamic systems**
Hadjicostis, C.; Verghese, G.C.;
Information Theory, 2000. Proceedings. IEEE International Symposium on
25-30 June 2000 Page(s):444
Digital Object Identifier 10.1109/ISIT.2000.866742

AbstractPlus | Full Text: PDF(100 KB)   IEEE CNF
Rights and Permissions

10. **Experimental comparison of voting algorithms in cases of disagreement**
Bass, J.M.; Latif-Shabgahi, G.; Bennett, S.;
EUROMICRO 97. 'New Frontiers of Information Technology'., Proceedings of the 23rd
EUROMICRO Conference
1-4 Sept. 1997 Page(s):516 - 523
Digital Object Identifier 10.1109/EURMIC.1997.617368

AbstractPlus | Full Text: PDF(596 KB)   IEEE CNF
Rights and Permissions

11. **Component-oriented voter model for dependable control applications**
Latif-Shabgahi, G.; Bass, J.M.; Bennett, S.;
Control '98. UKACC International Conference on (Conf. Publ. No. 455)
Volume 1, 1-4 Sept. 1998 Page(s):410 - 415 vol.1

AbstractPlus | Full Text: PDF(480 KB)   IEE CNF

Help   Contact Us   Privacy & Security   IEEE.org

Indexed by
 Inspec°